## Freeform Search

Database:	US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins				
Term:	memory and mount state				
Display:	10 Documents in Display Format: -	Starting with Number 1			
Generate: O Hit List O Hit Count O Side by Side O Image					
Search Clear Interrupt					
Search History					

DATE: Monday, January 30, 2006 Printable Copy Create Case

Set Name Query side by side		Hit Count Set Name result set	
•	GPB,USPT; PLUR=YES; OP=ADJ		
<u>L14</u>	5093797[uref]	13	<u>L14</u>
<u>L13</u>	(integrated circuit or ic) same mount state	6	<u>L13</u>
<u>L12</u>	(integrated circuit or ic) near7 mount state	0	<u>L12</u>
<u>L11</u>	L10 not 19	3	<u>L11</u>
<u>L10</u>	memory same mount state	6	<u>L10</u>
<u>L9</u>	memory near15 mount state	3	<u>L9</u>
<u>L8</u>	memory and mount state	67	<u>L8</u>
<u>L7</u>	20020103891 and mount state	1	<u>L7</u>
<u>L6</u>	20020103891	1	<u>L6</u>
<u>L5</u>	5809329.pn.	1	<u>L5</u>
<u>L4</u>	200400032245809329.pn.	0	<u>L4</u>
<u>L3</u>	20040003224	1	<u>L3</u>
<u>L2</u>	5867579[uref]	16	<u>L2</u>
<u>L1</u>	5867579.pn.	1	<u>L1</u>

END OF SEARCH HISTORY